jc929 U.S. PTO
09/788596
02/21/01

Subclass	Class	ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.
SCANNED KS TD3 Q.A TM

PATENT DATE

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/788596	F=	257	780	2811	PARIICH Thomas S

APPLICANTS: Hirokazu Miyazaki

TITLE: Mounting structure of semiconductor device and mounting method thereof

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PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)	
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)

INTERNATIONAL CLASSIFICATION

☒ Continued on Issue Slip inside File Jacket

☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
☐ The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
☐ The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	(Assistant Examiner) (Date)			ISSUE FEE	
☐ The terminal ____ months of this patent have been disclaimed.	(Primary Examiner) (Date)			Amount Due	Date Paid
	(Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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